Fig. 1

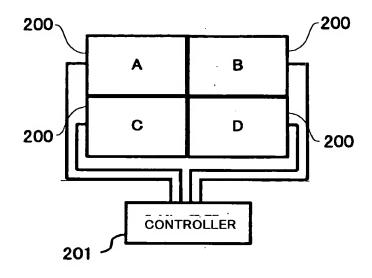


Fig.2 200 3 5 6 8 RGB ANALOG SIGNALS FRAME **IMAGE MEMORY** PROCESSOR DISPLAY VIDEO SIGNALS SELECTOR LD FRAME MEMORY CONTROLLER IMAGE PROCESSOR CONTROLLOR DISPLAY CONTROLLOR DIGITAL IMAGE SIGNALS INPUT FORMAT MEMORY DISPLAY PATTERN MEMORY OUTPUT FORMAT MEMORY 110 100 IMAGE SIGNALS O-9 10 INPUT FORMAT DETECTOR 2 **DISPLAY REGION CALCULATOR DISPLAY ADJUSTING SIGNAL**

Fig.3

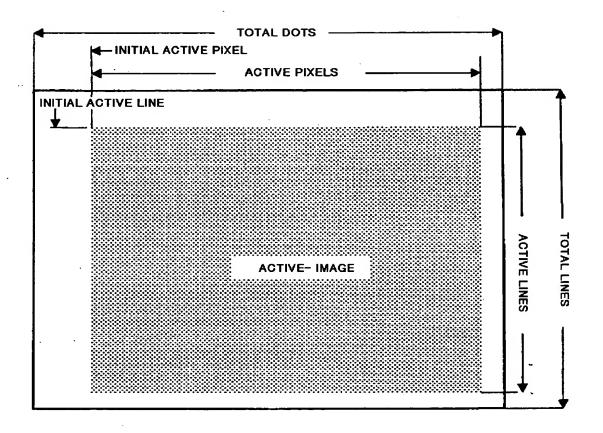


Fig.4

	INPUT MEMORY NUMBER			
INPUT FORMAT PARAMETERS	INPUT MEMORY 1	INPUT MEMORY 2	INPUT MEMORY 3	
TOTAL DOTS	1344 dots	1056 dots	858 dots	
TOTAL LINES	806 lines	816 lines	418 lines	
ACTIVE PIXELS	1024 dots	800 dots	720 dots	
ACTIVE LINES	768 lines	600 lines	480 lines	
INITIAL ACTIVE PIXEL	236 dots	216 dots	120 dots	
INITIAL ACTIVE LINE	35 lines	27 lines	40 lines	

Fig.5

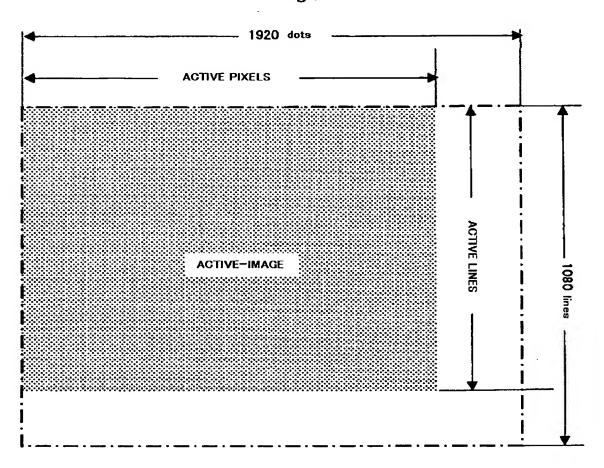


Fig.6

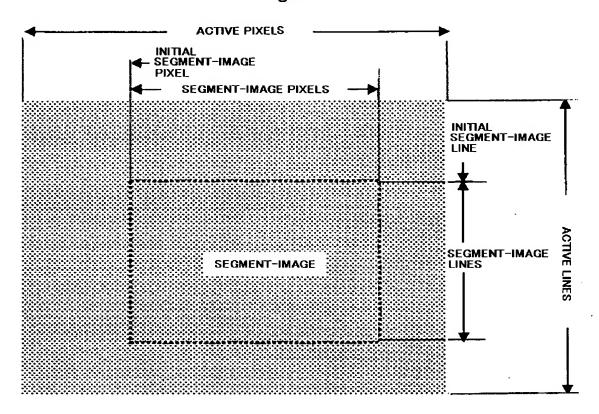
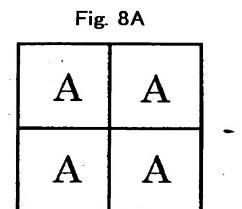


Fig.7

DISPLAY PARAMETERS	DISPLAY MEMORY NUMBER			
	DISPLAY MEMORY1	DISPLAY MEMORY:2	DISPLAY MEMORY'S	DISPLAY MEMORY4
INITIAL SEGMENT-IMAGE PIXEL .	0 dot	512 dots	() dot	400 dots
SEGMENT-IMAGE PIXELS	1024 dots	512 dots	.800 dots	400 dots
INITIAL SEGMENT-IMAGE LINE	0 line	384 lines	0 line	300 · lines
SEGMENT-IMAGE LINES	768 lines	384 lines	600 lines	300 lines
HORIZONTAL OFFSET	0 dot	0 dot	0 dot	0 dot
HORIZONTAL DISPLAY-IMAGE SIZE	1028 dots	1028 dots	1024 dots	1024 dots
VERTICAL OFFSET	0 line	0 line	0 line	() line
VERTICAL DISPLAY-IMAGE SIZE	772 lines	772 lines	768 lines	768 lines
INPUT MEMORY NUMBER	1	1	2	2



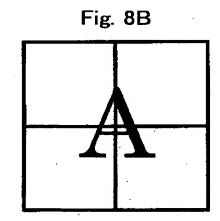


Fig.9

OUTPUT FORMAT PARMETERS	OUTPUT FORMAT	
DOT CLOCK	65MHz	
HORIZONTAL SYNCHRONOUS LINES	ß lines	
VERTICAL SYNCHRONOUS PIXELS	136 dots	
TOTAL DOTS	1344 dots	
TOTAL LINES	806 lines	
OUTPUT PIXELS	1024 dots	
OUTPUT LINES	768 lines	
INITIAL OUTPUT PIXEL	236 dots	
INITIAL OUTPUT LINE	35 lines	

Fig.10

